

**描述 / Descriptions**

SOP-8 塑封封装 带精密电压基准的双通道运算放大器。

BROP4358SC 是一款专门设计用于在电池充电器和开关电源应用上调节输出电流和电压的集成电路。器件内部包含了两个运算放大器和一个 2.5V 的精密基准电压。运放 1 的同相输入端与电压基准调节器的输出端相连，用于控制电压；运放 2 的输入端内部是无连接的，用于控制电流。在电源管理和数据采集系统等应用中，该器件的使用节省了空间和成本。

Dual Operational Amplifier and Voltage Reference in a SOP-8 Plastic Package.

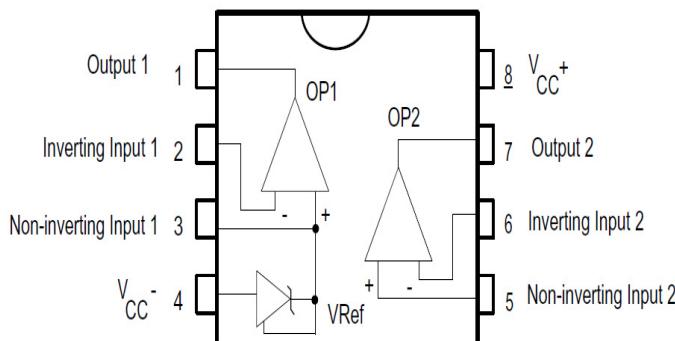
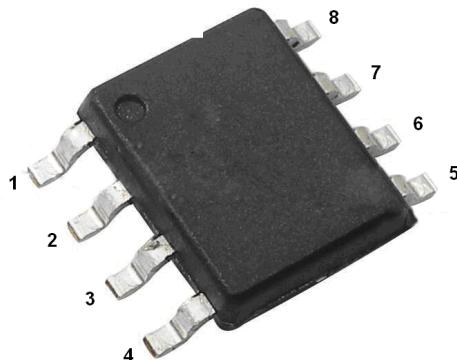
The BROP4358SC is a monolithic IC specifically designed to regulate the output current and voltage levels of battery chargers and switching power supplies. The device contains two Op Amps and a 2.5V precision shunt voltage reference. Op Amp 1 is designed for voltage control with its non-inverting input internally connects to the output of the shunt regulator. Op Amp 2 is for current control with both inputs uncommitted. This device is offering space and cost saving in many applications like power supply management or data acquisition systems.

**特征 / Features**

- ◆ 工作电压范围宽 ( VCC =3V ~36V )。
- ◆ 输出电压范围大 ( 0~Vcc-1.5V )。
- ◆ 输入共模电压接近地电平。
- ◆ 固定输出基准电压2.5V , 精度±0.4%和±0.8%。
- ◆ 阴极电流能力 : 1~100mA。
- ◆ 典型输出电阻 : 0.15Ω。
- ◆ Wide operating supply range (Vcc=3V~36V).
- ◆ Large output voltage swing: 0 to (Vcc-1.5V).
- ◆ Input common mode voltage includes ground.
- ◆ Fixed output Voltage reference: 2.5V, voltage tolerance: ±0.4% and ±0.8%.
- ◆ Sink current capability: 1 to 100mA.
- ◆ Typical output impedance: 0.15 Ω .

**用途 / Applications**

- ◆ 电池充电器。
- ◆ AC/DC适配器。
- ◆ LED照明。
- ◆ Battery charger.
- ◆ AC/DC power adapter.
- ◆ LED lightings.

**内部等效电路 / Equivalent Circuit****引脚排列 / Pinning**

PIN 见内部等效电路 / PIN See Equivalent Circuit.

**印章代码 / Marking**

见印章说明 / See Marking Instructions

**极限参数 / Absolute Maximum Ratings( $T_a=25^\circ\text{C}$ )**

参数 Parameter	符号 Symbol	数值 Rating	单位 Unit
Supply Voltage	$V_{CC}$	36	V
Differential input voltage	$V_{I(DIFF)}$	36	V
Input Voltage	$V_I$	-0.3 to +36	V
Operating Temperature	$T_{opr}$	-40 to +125	$^\circ\text{C}$
Maximum Junction Temperature	$T_J$	150	$^\circ\text{C}$

**电性能参数 / Electrical Characteristics(Ta=25°C) (V<sub>CC</sub><sup>+</sup> = +5V, V<sub>CC</sub> = Ground ,Vo = 1.4V)**

参数 Parameter	符号 Symbol	测试条件 Test Conditions	最小值 Min	典型值 Typ	最大值 Max	单位 Unit
Supply Current	I <sub>CC</sub>	V <sub>CC</sub> =5V		0.5	1.2	mA
		V <sub>CC</sub> =30V		0.8	2.0	mA
<b>Operator2(independent op-amp)</b>						
Input offset voltage	V <sub>IO</sub>	V <sub>CM</sub> =0 to V <sub>CC</sub> -1.5 V <sub>O(p)</sub> =1.4V    R <sub>S</sub> =0		1	3	mV
Input Offset Voltage Drift	D <sub>VO</sub>	T <sub>min</sub> ≤T <sub>amb</sub> ≤ T <sub>max</sub>		7		UV/°C
Input offset current	I <sub>IO</sub>	T <sub>A</sub> =25°C		2.0	30	nA
Input Bias current	I <sub>Bias</sub>	T <sub>A</sub> =25°C		45	100	nA
Large signal Voltage Gain	Avd	V <sub>CC</sub> =15V    R <sub>L</sub> =2kΩ V <sub>O</sub> = 1.4V to 11.4V	25	100		V/mV
Supply Voltage Rejection Ratio	SVR	V <sub>CC</sub> = 5V to 30V	65	100		dB
Input Common Mode Voltage Range	V <sub>ICM</sub>	V <sub>CC</sub> = +30V - see note <sup>1)</sup>	0		(V <sub>CC</sub> <sup>+</sup> ) -1.5	V
Common mode rejection Ratio	CMR	T <sub>A</sub> =25°C	70	85		dB
Output Current Source	I <sub>source</sub>	V <sub>CC</sub> = +15V    V <sub>O</sub> = 2V V <sub>ID</sub> = +1V	20	40		mA
Short Circuit to Ground	I <sub>SC</sub>	V <sub>CC</sub> = +15V		40	60	mA
Output Current Sink	I <sub>sink</sub>	V <sub>CC</sub> = +15V    V <sub>O</sub> = 2V V <sub>ID</sub> = -1V	10	13		mA
High Level Output Voltage	V <sub>OH</sub>	V <sub>CC</sub> +=30V    T <sub>amb</sub> =25°C R <sub>L</sub> =10kΩ	27	28		V
Low Level Output Voltage	V <sub>OL</sub>	R <sub>L</sub> = 10k		5	20	mV
Slew Rate at Unity Gain	SR	V <sub>i</sub> = 0.5 to 3V    V <sub>CC</sub> = 15V R <sub>L</sub> = 2kΩ    C <sub>L</sub> = 100pF unity gain	0.2	0.4		V/μS
Gain Bandwidth Product	GBP	V <sub>CC</sub> = 30V    R <sub>L</sub> = 2kΩ CL=100pF    f=100kHz V <sub>in</sub> =10mV	0.5	0.9		MHz
Total Harmonic Distortion	THD	f=1kHz    A <sub>V</sub> =20dB R <sub>L</sub> =2kΩ    V <sub>CC</sub> =30V C <sub>L</sub> =100pF    V <sub>O</sub> = 2V <sub>pp</sub>		0.02		%

**电性能参数 / Electrical Characteristics(Ta=25°C) (V<sub>CC</sub><sup>+</sup> = +5V, V<sub>CC</sub> = Ground, V<sub>O</sub> = 1.4V)**

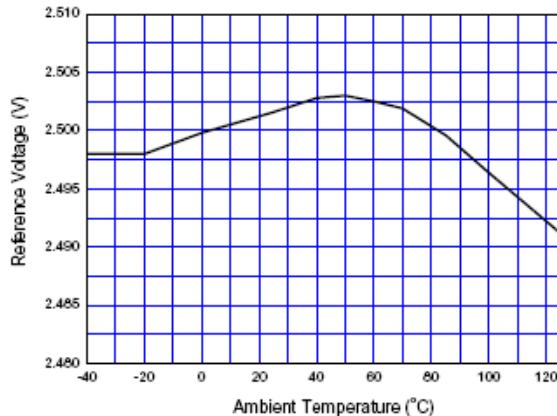
<b>Operator 1 (op-amp with non-inverting input connected to the internal Vref)</b>						
Input offset voltage	V <sub>IO</sub>	V <sub>Cm</sub> =0 to V <sub>CC</sub> -1.5 V <sub>O(p)</sub> =1.4V    R <sub>S</sub> =0		1	3	mV
Input Offset Voltage Drift	D <sub>VO</sub>	T <sub>min</sub> ≤T <sub>amb</sub> ≤ T <sub>max</sub>		7		UV/ °C
Input Bias current	I <sub>BIAS</sub>	T <sub>a</sub> =25°C		45	100	nA
Large signal Voltage Gain	Avd	V <sub>CC</sub> =15V    R <sub>L</sub> =2kΩ V <sub>O</sub> = 1.4V to 11.4V	25	100		V/mV
Supply Voltage Rejection Ratio	SVR	V <sub>CC</sub> = 5V to 30V	65	100		dB
Output Current Source	I <sub>source</sub>	V <sub>CC</sub> = +15V    V <sub>O</sub> = 2V V <sub>id</sub> = +1V	20	40		mA
Short Circuit to Ground	I <sub>SC</sub>	V <sub>CC</sub> = +15V		40	60	mA
Output Current Sink	I <sub>sink</sub>	V <sub>CC</sub> = +15V    V <sub>O</sub> = 2V V <sub>id</sub> = -1V,	10	13		mA
High Level Output Voltage	V <sub>OH</sub>	V <sub>CC</sub> + =30V    T <sub>amb</sub> = 25°C R <sub>L</sub> = 10kΩ	27	28		V
Low Level Output Voltage	V <sub>OL</sub>	R <sub>L</sub> = 10k		5	20	mV
Slew Rate at Unity Gain	SR	V <sub>i</sub> = 0.5 to 3V    V <sub>CC</sub> = 15V R <sub>L</sub> = 2kΩ    C <sub>L</sub> = 100pF unity gain	0.2	0.4		V/μS
Gain Bandwidth Product	GBP	V <sub>CC</sub> = 30V    R <sub>L</sub> = 2kΩ C <sub>L</sub> = 100pF    f = 100kHz V <sub>in</sub> = 10mV	0.5	0.9		MHz
Total Harmonic Distortion	THD	f = 1kHz    A <sub>V</sub> = 20dB R <sub>L</sub> = 2kΩ    V <sub>CC</sub> = 30V C <sub>L</sub> = 100pF    V <sub>O</sub> = 2V <sub>pp</sub>		0.02		%
<b>Voltage Reference</b>						
Reference Input Voltage	Vref	VKA=VREF IK=10mA(A=0.4%)	2.49	2.5	2.51	V
		VKA=VREF IK=10mA(B=0.8%)	2.48	2.5	2.52	V
Reference Input Voltage Deviation Over Temperature Range	△Vref	VKA = Vref    I <sub>k</sub> =10mA T <sub>min</sub> ≤T <sub>amb</sub> ≤ T <sub>max</sub>		7	30	mV
Minimum Cathode Current for Regulation	I <sub>k(min)</sub>	VKA = Vref		0.4	1	mA
Dynamic Impedance - note <sup>2)</sup>	ZKA	VKA = Vref IK = 1 to 100mA f < 1kHz		0.15	0.5	Ω

1. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is V<sub>CC</sub><sup>+</sup> - 1.5V.

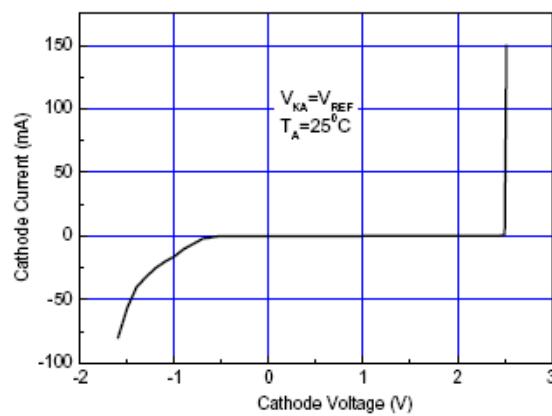
But either of both inputs can go to +36V without damage.

2. The dynamic impedance is defined as |ZKA| = △VKA/△IK.

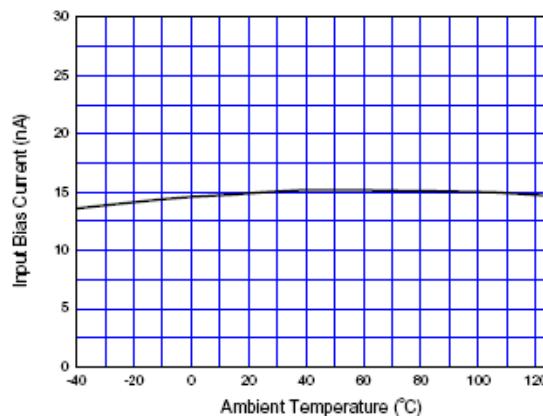
## 电参数曲线图 / Electrical Characteristic Curve



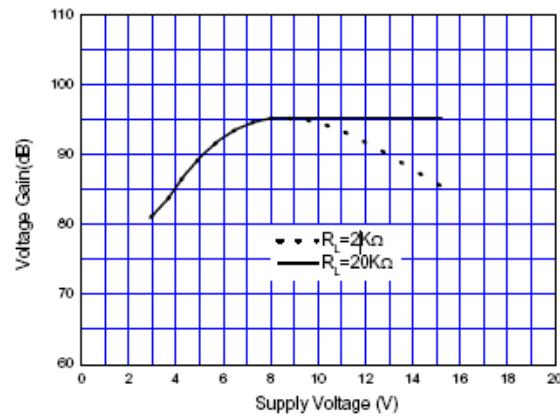
Reference Voltage vs. Ambient Temperature



Cathode Current vs. Cathode Voltage

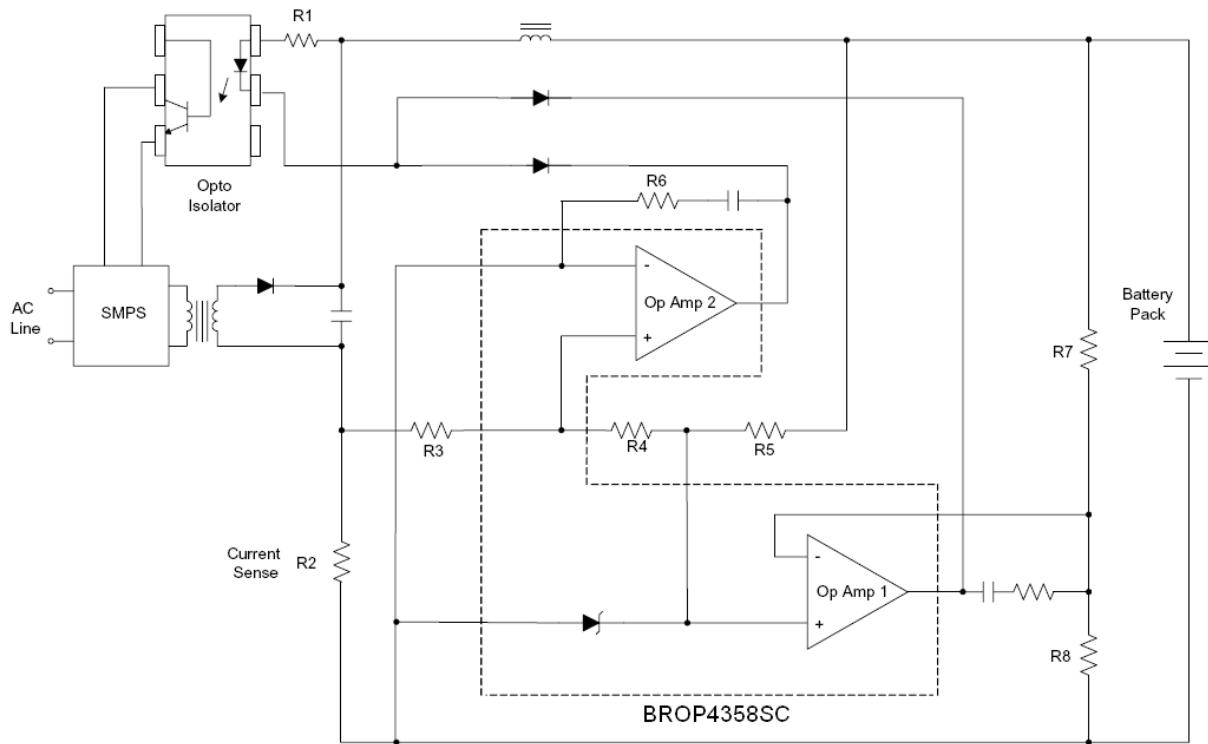


Input Bias Current vs. Ambient Temperature



Op Amp Voltage Gain

## 典型应用 / Typical Application

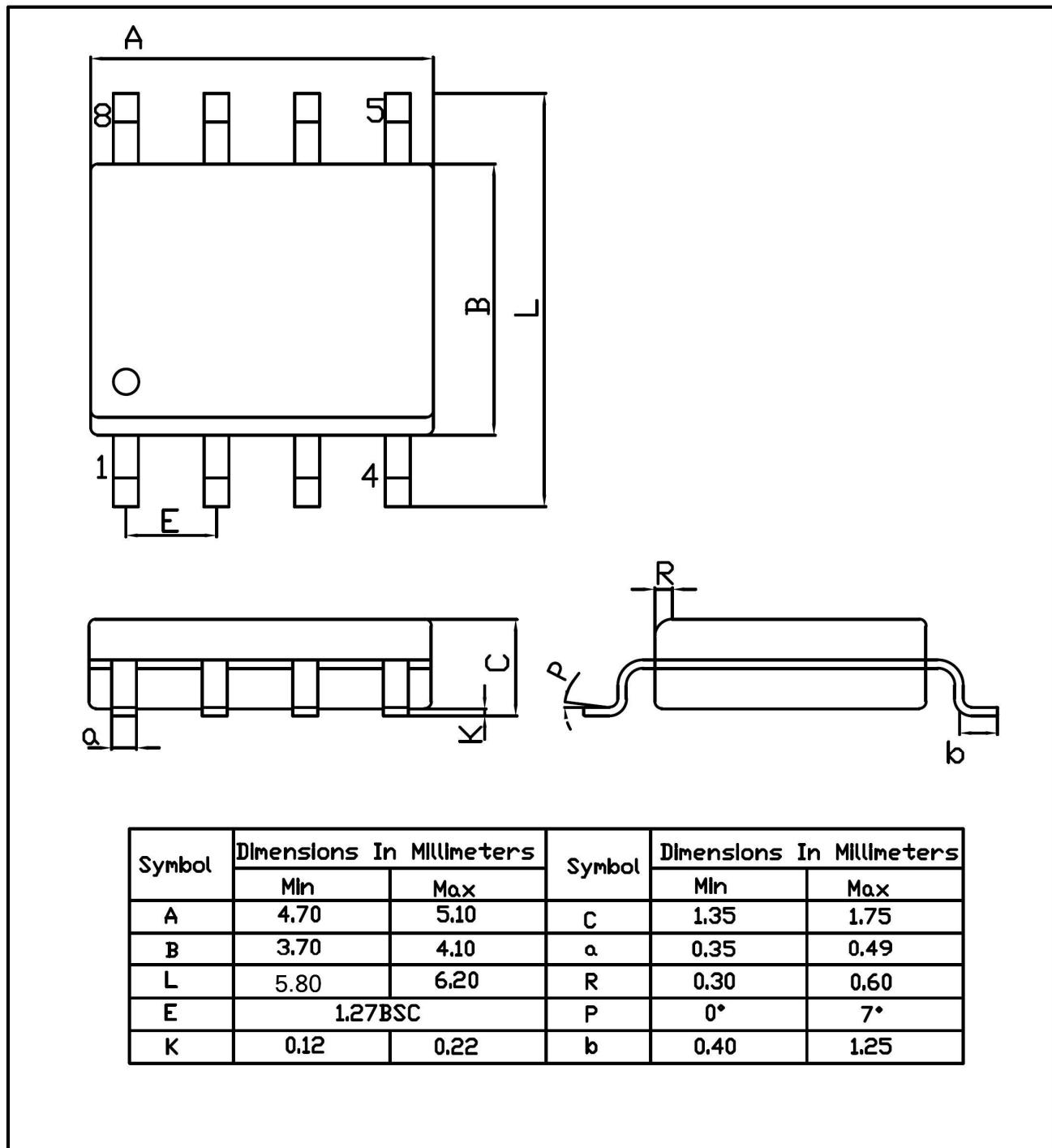


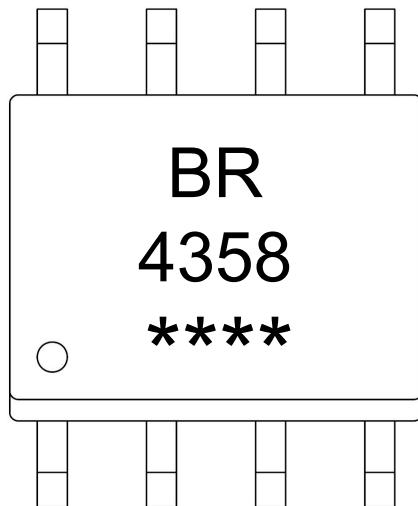
Application of BROP4358SC in a Constant Current and Constant Voltage Charger

## 外形尺寸图 / Package Dimensions

SOP-8

Unit:mm



**印章说明 / Marking Instructions**

说明：

BR : 为公司代码

4358 : 为型号代码

\*\*\*\* : 为生产批号代码，随生产批号变化

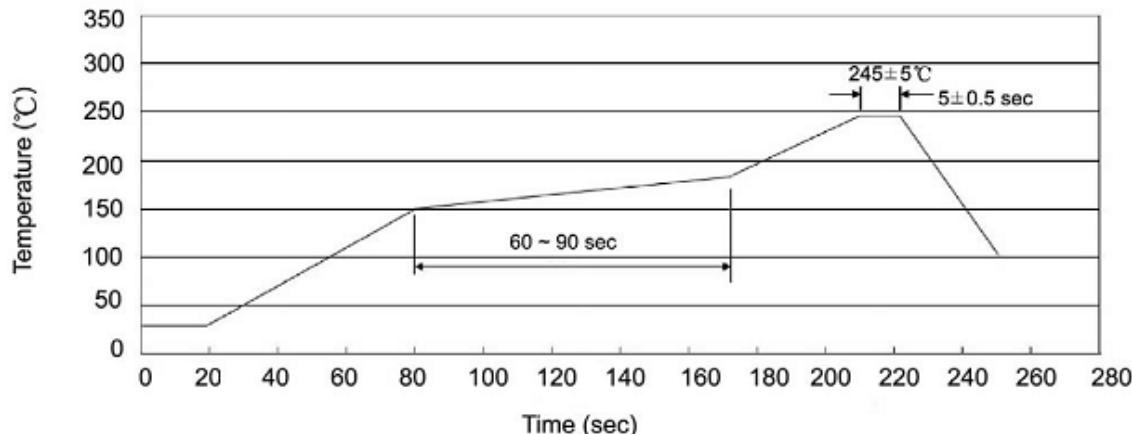
Note:

BR: Company Code.

4358: Product Type.

\*\*\*\*: Lot No. Code, code change with Lot No.

**回流焊温度曲线图(无铅) / Temperature Profile for IR Reflow Soldering(Pb-Free)**



说明：

- 1、预热温度 150 ~ 180°C , 时间 60 ~ 90sec;
- 2、峰值温度 245±5°C , 时间持续为 5±0.5sec;
- 3、焊接制程冷却速度为 2 ~ 10°C/sec.

Note:

- 1.Preheating:150~180°C, Time:60~90sec.
- 2.Peak Temp.:245±5°C, Duration:5±0.5sec.
3. Cooling Speed: 2~10°C/sec.

**耐焊接热试验条件 / Resistance to Soldering Heat Test Conditions**

温度 : 260±5°C

时间 : 10±1 sec.

Temp.:260±5°C

Time:10±1 sec

**包装规格 / Packaging SPEC.**

卷盘包装 / REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm <sup>3</sup> )		
	Units/Reel 只/卷盘	Reels/Inner Box 卷盘/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Reel	Inner Box 盒	Outer Box 箱
SOP/ESOP-8	4,000	2	8,000	6	48,000	13" ×12	360×360×50	380×335×366

**使用说明 / Notices**